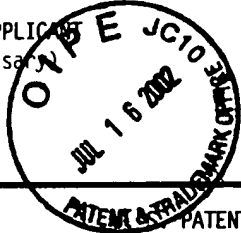


FORM PTO-1449  
(REV. 7.80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
00-BN-055SERIAL NO.  
09/751,377LIST OF REFERENCES CITED BY APPLICANT  
(Use several sheets if necessary)APPLICANT  
Anthony X. JarvisFILING DATE  
December 29, 2000GROUP  
2183

## PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
B50	AA	5.872.986	02/16/1999	Heeb	395	800.01	09/30/1997
B50	AB	6.078.196	06/20/2000	Gayles	326	121	09/17/1997

RECEIVED

JUL 19 2002

Technology Center 2100

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
B50	AC	EP 0 730 223 A1	09/04/1996	EPO				

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

B50	AD	TRAINIS, Simon A.; "MODELLING THE HARDWARE COST OF FULL REGISTER BYPASSING IN A MULTIPLE INSTRUCTION ISSUE PROCESSOR"; XP 000679755; Journal of Systems Architecture; 1997 Published by Elsevier Science B.V.; Pgs. 39-46

EXAMINER

DATE CONSIDERED

12/5/03

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.